

FIG.2

SYSTEM AND METHOD FOR DETERMINING... Shimazaki et al. Greer, Burns & Crain, Ltd. Ref. No. 1122.70096 Sheet 3 of 9

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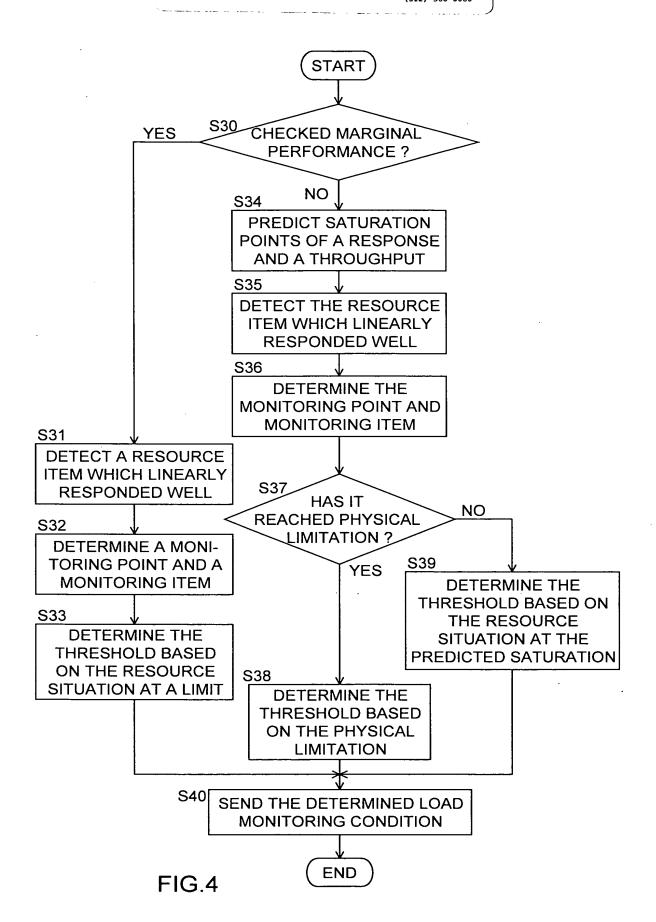
sar -u 5 5				
	%usr	%sys	%wio	%idle
09:36:25		· · · · · · · · · · · · · · · · · · ·		
09:36:30	0	0	0	100
09:36:35	0	1	1	98
09:36:40	0	0.	. 0	100
09:36:45	0	. 0	0	100
09:36:50	0	1	0	99
				•
Average	0	0	0	99

FIG.3A

sar -r 5 5		
	freemem	freeswap
09:45:00		
09:45:05	3820	1104608
09:45:10	3792	1104054
09:45:15	3788	1102528
09:45:20	3825	1105024
09:45:25	3836	1105024
Average	3812	1104248

FIG.3B

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TEST		CPU			MEMOR					0/1	i						<b>\</b>							

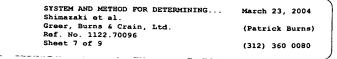
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1.22 1.02 1.44 4.98 5.34 5.21 6.23 5.32 4.75 0.19 0.67 0.21 1.31 RATE OF CHANGE 33 27 27 27 60 60 60 60 60 7 VARIATION TEST c TEST b TEST a freeswap sml\_mem freemem lg\_mem %pasy blks/s r+w/s avque %sys %wio %idle alloc SERVER B MEMORY CPU 9

FIG.6



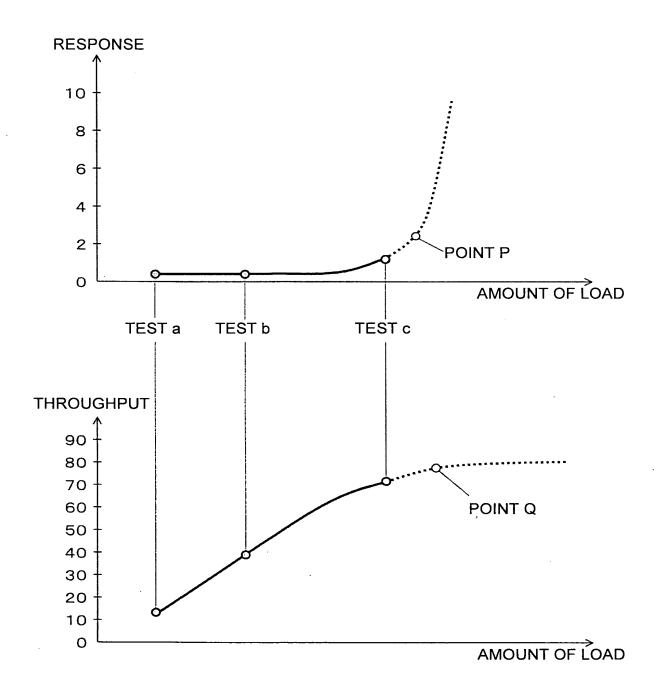
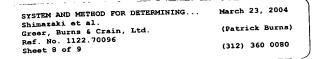


FIG.7



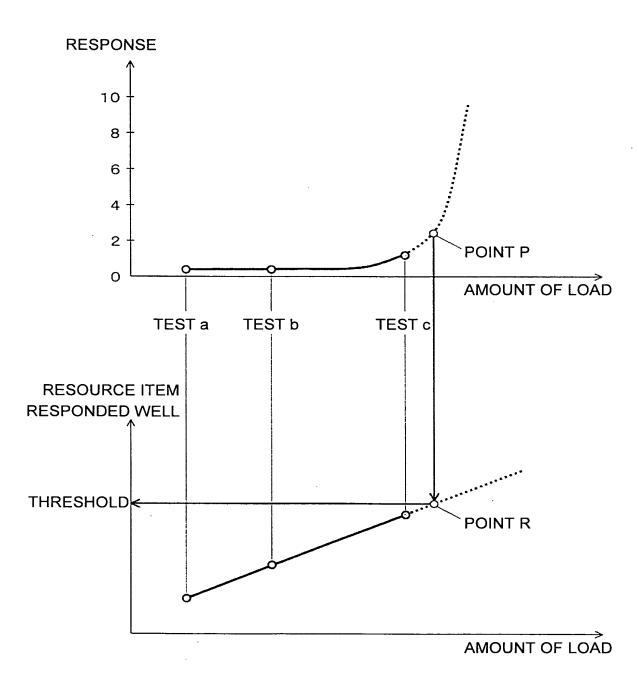


FIG.8

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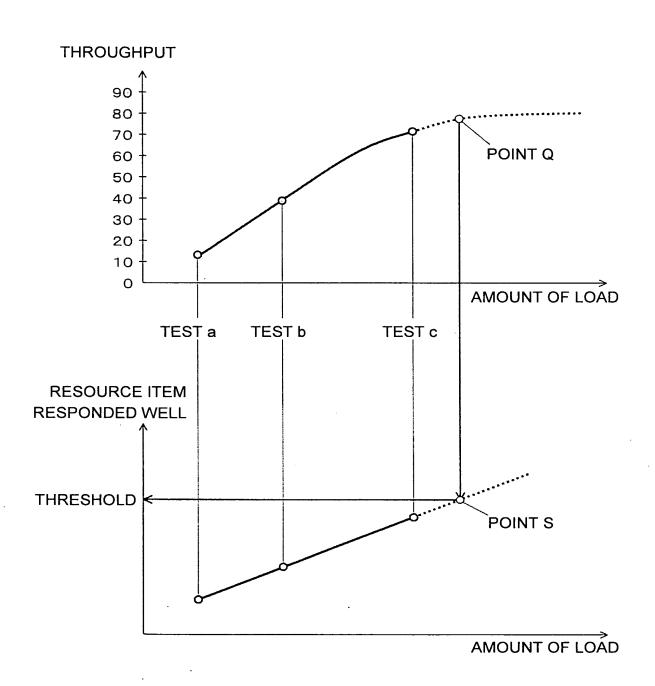


FIG.9